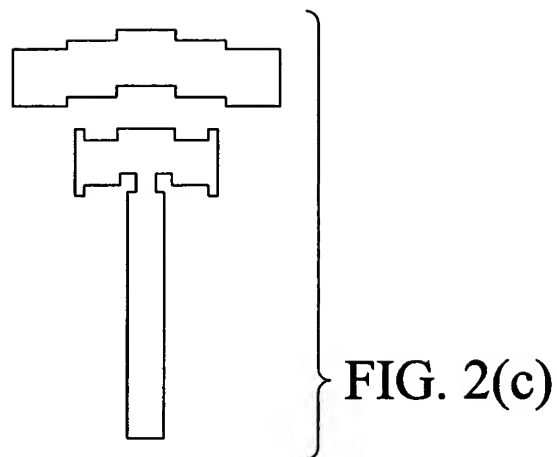
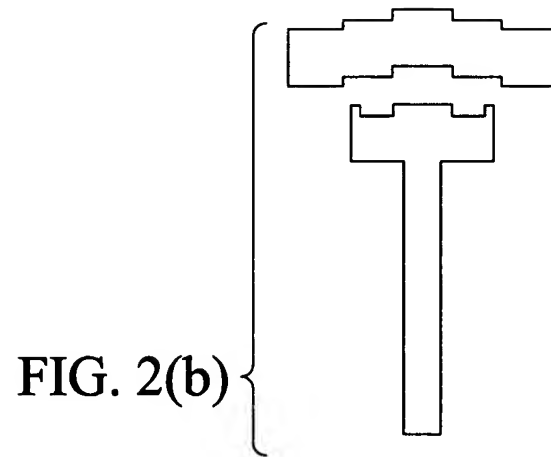
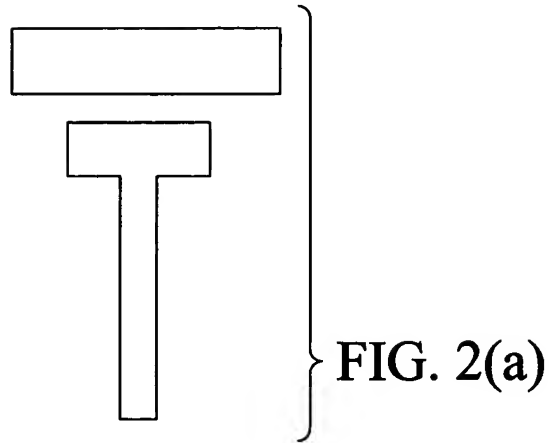


FIG. 1



GATE SIZING PROBLEM	MINIMUM COST OF CORRECTION PROBLEM
AREA	COST OF CORRECTION
NOMINAL DELAY	DELAY $\mu + k\sigma$
CYCLE TIME	SELLING POINT DELAY
DIE AREA	TOTAL COST OF RET

FIG. 3